

Attorney Docket No. 15162/01290

JCS42 U.S. PTO

09/438247



11/12/99

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re

U.S. application: Junji NISHIGAKI, Atsushi ISHIKAWA,
Kenichi SAWADA, Kazuhiro ISHIGURO
and Hiroyuki SUZUKI

For: IMAGE PROCESSOR PROCESSING IMAGE
DATA IN PARALLEL WITH A PLURALITY OF
PROCESSORS

U.S. Serial No.: Not Yet Assigned

Filed: Concurrently

Group Art Unit: Not Yet Assigned

Examiner: Not Yet Assigned

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Assistant Commissioner

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Box Patent Application

Washington, D.C. 20231

Derrick Gordon

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fee)

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November 12, 1999

Date of Signature

Dear Sir:

INFORMATION DISCLOSURE STATEMENT

In compliance with the duty of disclosure set forth
in 37 C.F.R. § 1.56, Applicants wish to bring the

following item to the attention of the Examiner. A copy of the document is enclosed for the convenience of the Examiner.

No representation is made, and no representation is intended, that more relevant material does not exist, or that the order of presentation of this material in any way reflects its relative pertinence. The references cited below are not intended to constitute an admission of any kind. Specifically, this presentation is not an admission that the item listed below are properly citable against the above-identified application.

The following document is cited in the specification of the above-identified patent application:

- (1) Japanese Published Unexamined Patent
Application (A) No. 03-177961 (MITSUBISHI
ELECTRIC CORP), published August 1, 1991.

Applicants consider the invention to be distinguishable over the above-cited document.

As this Information Disclosure Statement is being filed concurrently with the filing of the above-referenced patent application, no fee is incurred. However, if it should be determined that a fee is required, please charge any required fee (other than the issue fee) during the

Attorney Docket No. 15162/01290

pendency of this application to Deposit Account No.
18-1260.

Respectfully submitted,

By: James W. Williams
James W. Williams
Registration No. 20,047
Attorney for Applicants

JWW/fis

SIDLEY & AUSTIN
717 North Harwood
Suite 3400
Dallas, Texas 75201-6507
(214) 981-3328 (direct)
(214) 981-3300 (main)
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